7.Proposed amendments to Recommendation 0.151

Change the title of the Recommendation as follows:

"ERROR PERFORMANCE MEASURING EQUIPMENT FOR DIGITAL SYSTEMS AT THE PRIMARY RATE AND ABOVE".

In the preamble and section 1 General replace the words, bit error ratio by: "error performance".

Renumber existing § 2.3 to 2.4 and insert a new § 2.3 to read:

"2.3Pseudorandom pattern for systems using 2^{20}-1 pattern length

This pattern may be generated by a twenty stage shift register with feedback taken from the seventeenth and twentieth stages. The output signal is taken from the twentieth stage, and an output bit is forced to be a "one" whenever the next 14 bits are all "zero".
The quasi-random sequence satisfies the following:

Modify Table 2/0.151 to show both bit patterns 2^{15} -1 and 2^{20} -1 to be specified for the 1544, 6312, 32064 and 44736 bit rates.